

**INTEGRATED TRANSMITTERS AND LONG-WAVELENGTH
VCSELs**

ONR Grant No. N00014-96-1-0024

Annual Progress Report
For the period:

October 1, 2000 – September 30, 2001

Submitted to

Dr. Yoon Soo Park
Office of Naval Research
Program Director
Optoelectronics
800 N. Quincy St.
Arlington, VA 22217-5660

By

Pallab Bhattacharya

Solid State Electronics Laboratory
Department of Electrical and Computer Engineering
The University of Michigan
Ann Arbor, MI 48109-2122
Telephone: 734-763-6678

REPORT DOCUMENTATION PAGE			Form Approved OMB NO. 0704-0188	
Public reporting burden for this collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comment regarding this burden estimates or any other aspect of this collection of information, including suggestions for reducing this burden to Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Suite 1204, Arlington, VA 22202-4302, and to the Office of Management and Budget, Paperwork Reduction Project (0704-0188), Washington, DC 20503.				
1. AGENCY USE ONLY (Leave blank)		2. REPORT DATE 10/11/2001		3. REPORT TYPE AND DATES COVERED Progress Report 10/1/00-9/30/01
4. TITLE AND SUBTITLE InP-Based Monolithically Integrated Transmitters Integrated Transmitters & Long Wavelength VCSELs			5. FUNDING NUMBERS N00014-96-1-0024	
6. AUTHOR(S) Pallab Bhattacharya				
7. PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) The University of Michigan Department of Electrical Engineering and Computer Science 1301 Beal Ave. Ann Arbor, MI 48109-2122			8. PERFORMING ORGANIZATION REPORT NUMBER	
9. SPONSORING / MONITORING AGENCY NAMES(S) AND ADDRESS(ES) Office of Naval Research 800 North Quincy Street Arlington, VA 22217-5660			10. SPONSORING / MONITORING AGENCY REPORT NUMBER	
11. SUPPLEMENTARY NOTES The views, opinions and/or findings contained in this report are those of the author(s) and should not be construed as an official Department of the Army position, policy or decision, unless so designated by other documentation.				
12a. DISTRIBUTION / AVAILABILITY STATEMENT Approved for public release; distribution unlimited.			12b. DISTRIBUTION CODE	
13. ABSTRACT (Maximum 200 words) The objective of this program is to develop InP-based monolithically integrated transmitters, including internal (current) and external modulation. The basic issues involved in such integration and operation of the individual devices will be explored both theoretically and experimentally. Thus, intrinsic and extrinsic factors that limit internal and external modulation, propagation and scattering of light in guided structures and through mirrors, and circuits, materials and lithography issues to develop high-frequency (>30 GHz) transmitters will be explored. Integrated chips with driver circuits and guided wave elements will be developed and tested. At the same time we are also developing novel top- and edge-emitting microcavity laser structures in which zero or very low threshold currents are expected due to phonon confinement. Preliminary results, both theoretical and experimental, are very encouraging, and we envisage that these low threshold, high frequency devices will be extremely useful for chip-to-chip and array-based optical interconnects.				
14. SUBJECT TERMS			15. NUMBER OF PAGES	
			16. PRICE CODE	
17. SECURITY CLASSIFICATION OR REPORT UNCLASSIFIED	18. SECURITY CLASSIFICATION OF THIS PAGE UNCLASSIFIED	19. SECURITY CLASSIFICATION OF ABSTRACT UNCLASSIFIED	20. LIMITATION OF ABSTRACT UL	

Objective

To design, fabricate and characterize microcavity lasers and LEDs and 1.55 μ m monolithically integrated transmitters.

Progress

1. 1.55 μ m InP-based Photonic Crystal microcavities

1.1 Introduction

Microcavities with dimensions on the wavelength scale are being extensively investigated due to their ability to exhibit enhanced spontaneous emission, directional output and single-mode operation. Photonic crystals with single or multiple defects have emerged as the preferred way to obtain such microcavities¹⁻³. We have designed and fabricated a 1.55 μ m electrically injected photonic crystal microcavity using an InP-based heterostructure.

1.2 Fabrication and Design

The InP-based laser heterostructure is grown by MOCVD. It consists of an n+ InP contact layer, an n-type bottom InP/InGaAsP distributed Bragg reflector (DBR) mirror, an undoped 1.55 μ m λ -cavity with 9 pseudomorphic quantum wells in the middle and p-type InGaAsP and contact layers on the top. A p-type AlInAs layer are also inserted on top of the cavity region for eventual lateral wet-oxidation during the processing of the device. The photoluminescence emission peak from the quantum wells is observed at 1.55 microns. Standard oxide-confined VCSEL process steps were used in the device fabrication with a combination of optical lithography, dry and wet etching, metallization and polyimide planarization. The 2D photonic crystal formation is achieved afterwards using the Leica/Cambridge EBMF ebeam lithography tool, a metal-on-polyimide process⁴ to reverse the ebeam pattern followed by anisotropic high temperature dry etching of the pattern into the InP heterostructure using a Plasmaquest ECR⁵ source. The deep etch goes through the entire cavity region. A single or multiple defects in the center define the lambda-sized microcavity which was designed with the photonic bandgap encompassing the peak emission wavelength of 1.55micron.

A photo micrograph of the top of the device with contact metallizations and SEM micrographs of the topview and cross-section of the 2D photonic crystal are shown in figures 1 and 2. The active area aperture, created by the single defect is 0.8microns. This aperture is surrounded by 65 periods of the photonic crystal, having an extent (radius) of 30 microns.

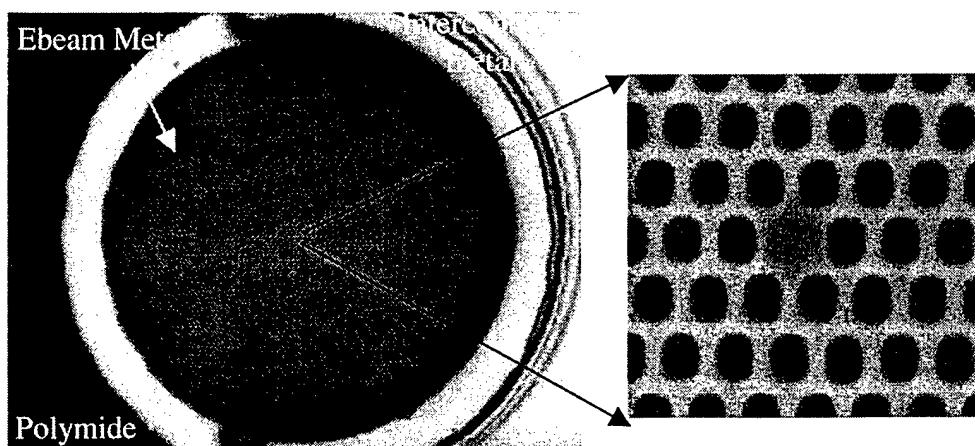


Fig 1. Photomicrograph of the topview of surface emitting device after standard VCSEL process with single defect photonic crystal ebeam pattern integrated in the center of the aperture. Top view SEM of single defect photonic crystal after ebeam patterning, pattern reversal and deep etching with hole radius of $0.2\mu\text{m}$ and pitch of $0.6\mu\text{m}$ a depth of $1.46\mu\text{m}$

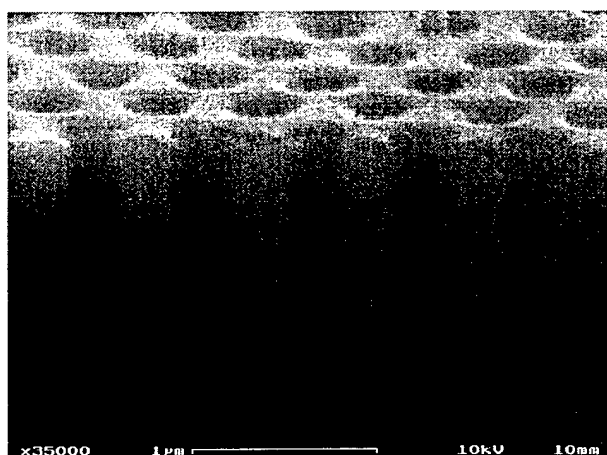


Fig 2. Cross Section SEM of InP photonic crystal after ebeam patterning, pattern reversal and deep etching with the Plasmaquest ECR source

1.3 Future Work

Initial L-I measurements were taken before the 2D photonic crystal formation. Measurements of the device with 2D photonic crystal microcavity are currently in progress.

2. Long-wavelength vertical cavity surface emitting lasers on patterned GaAs substrates

2.1 Summary

The progress of the project on long-wavelength vertical cavity surface emitting lasers (VCSELs) on patterned GaAs substrates is reported next. First, the prospects and advantages of the present scheme on other long-wavelength VCSELs is reviewed and restated. Second, the design and layout of the novel VCSELs are discussed, followed by the recent progress in the fabrication of the devices. Finally, the current and future phases of the project are shortly outlined.

2.2 Introduction

Long-wavelength (1.30-1.55 μm) VCSELs are one of the most promising light sources for long-haul fiber optic communications. Conventional VCSELs with InGaAsP wells grown on InP substrates utilize lattice-matched InP/InGaAsP (or InAlGaAs) distributed Bragg reflectors (DBRs), the refractive index contrast of which is so low that over 40 DBR periods is required to obtain a high reflectance in the vicinity of the lasing wavelength. Consequently, epitaxial and processing challenges arise, and the high resistivity of the thick DBRs strongly degrades the performance of these VCSELs.

Higher refractive index contrasts can be achieved with amorphous dielectric mirrors (e.g. ZnSe/MgF) or by fusing AlGaAs/GaAs DBRs to InP-based InGaAsP active layers. However, dielectric mirrors suffer low electrical conductivity, while the fusing technique has reliability concerns at the fused junctions and is not easily applicable to large wafers. To overcome many of the mentioned challenges, growth on patterned substrates has been pursued in this group.

AlGaAs/GaAs system is largely lattice-mismatched to InP/InGaAsP and the critical thickness is too low to grow DBR layers on InP substrates. Theoretical and experimental results, however, have proved that critical thickness is remarkably higher, and the misfit dislocations are considerably lower, if lattice-mismatched materials are grown on patterned substrates (mesas or grooves). Our group has previously realized the growth of high quality GaAs/AlAs DBRs on top of patterned InP-based quantum well (QW) structures⁶, and has also demonstrated 1.55 μm InP-based VCSELs with short-stack defect-free GaAs-based top DBRs⁷. These have motivated the design, fabrication, and realization of InP/InGaAsP active layer VCSELs with AlGaAs/GaAs DBRs grown on patterned GaAs substrates. This novel scheme envisages many advantages to the previous work: (a) short-stack AlGaAs/GaAs DBRs can be grown not only for top p-doped DBRs, but also for the bottom n-doped distributed mirror; (b) growth can be accomplished in one step on a patterned substrate, i.e., no regrowth is required; (c) Misfit dislocations at the regrown interface are avoided.

λ	DBR	GaAs	0.1150 μm	$5 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	x5
		$\text{Al}_{0.8}\text{Ga}_{0.2}\text{As}$	0.1296 μm	$5 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		GaAs contact	0.2730 μm	$8 \times 10^{18} \text{ cm}^{-3} \text{ (p+)}$	
		AlGaAs graded	0.0300 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		$\text{Al}_{0.98}\text{Ga}_{0.02}\text{As}$	0.1286 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		AlGaAs graded	0.0300 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
λ cavity	λ cavity	GaAs	0.0200 μm		x26
		$\text{In}_{0.78}\text{Ga}_{0.22}\text{As}_{0.479}\text{P}_{0.521}$	0.1676 μm		
		$\text{In}_{0.485}\text{Ga}_{0.515}\text{As}_{0.83}\text{P}_{0.17}$	7 nm (barrier)		
		$\text{In}_{0.76}\text{Ga}_{0.24}\text{As}_{0.83}\text{P}_{0.17}$	8 nm (well)		
		$\text{In}_{0.485}\text{Ga}_{0.515}\text{As}_{0.83}\text{P}_{0.17}$	7 nm (barrier)		
		$\text{In}_{0.78}\text{Ga}_{0.22}\text{As}_{0.479}\text{P}_{0.521}$	0.1676 μm		
DBR	DBR	GaAs contact	0.1150 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	x26
		$\text{Al}_{0.8}\text{Ga}_{0.2}\text{As}$	0.1296 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	
		GaAs	0.1150 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	
		Patterned (001) GaAs substrate and buffer layers			

(a)

$3\lambda/4$	DBR	GaAs	0.1150 μm	$5 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	x5
		$\text{Al}_{0.8}\text{Ga}_{0.2}\text{As}$	0.1296 μm	$5 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		GaAs contact	0.1580 μm	$8 \times 10^{18} \text{ cm}^{-3} \text{ (p+)}$	
		AlGaAs graded	0.0300 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		$\text{Al}_{0.98}\text{Ga}_{0.02}\text{As}$	0.1286 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
		AlGaAs graded	0.0300 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (p)}$	
λ cavity	λ cavity	GaAs	0.0200 μm		x26
		$\text{In}_{0.78}\text{Ga}_{0.22}\text{As}_{0.479}\text{P}_{0.521}$	0.1707 μm		
		InP	7 nm (barrier)		
		$\text{In}_{0.76}\text{Ga}_{0.24}\text{As}_{0.83}\text{P}_{0.17}$	8 nm (well)		
		InP	7 nm (barrier)		
		$\text{In}_{0.78}\text{Ga}_{0.22}\text{As}_{0.479}\text{P}_{0.521}$	0.1707 μm		
DBR	DBR	GaAs contact	0.1150 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	x26
		$\text{Al}_{0.8}\text{Ga}_{0.2}\text{As}$	0.1296 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	
		GaAs	0.1150 μm	$2 \times 10^{18} \text{ cm}^{-3} \text{ (n)}$	
		Patterned (001) GaAs substrate and buffer layers			

(b)

Figure 3: The schematic of patterned substrate VCSELs: (a) with InGaAsP barrier QWs; (b) with InP barrier QWs.

2.3 Design

1.30- and 1.55- μm VCSELs with AlGaAs/GaAs DBRs and InGaAsP QWs were designed by an in-house two-dimensional optical mode simulator, which calculates different transverse electric (TE) and transverse magnetic (TM) modes of semiconductor waveguide structures. Two variations of devices with different barriers, namely InP and $\text{In}_{0.485}\text{Ga}_{0.515}\text{As}_{0.83}\text{P}_{0.17}$, were simulated. The layout of the two type of VCSELs are shown in Fig. 3 (a) and (b)¹ As can be seen, both VCSELs consists of 26 periods of $\text{Al}_{0.80}\text{Ga}_{0.20}\text{As}/\text{GaAs}$ quarter-wavelength λ n-doped DBRs, followed by a λ -cavity active layer with eight $\text{In}_{0.76}\text{Ga}_{0.24}\text{As}_{0.83}\text{P}_{0.17}/\text{In}_{0.485}\text{Ga}_{0.515}\text{As}_{0.83}\text{P}_{0.17}$ or $\text{In}_{0.76}\text{Ga}_{0.24}\text{As}_{0.83}\text{P}_{0.17}/\text{InP}$ QWs. Similar to the bottom mirror, the top one consists of five periods of $\lambda/4$ $\text{Al}_{0.80}\text{Ga}_{0.20}\text{As}/\text{GaAs}$ DBRs, while a λ -thick region² is placed between the top DBR and the λ -cavity active region. The extra layers are for p-contact purposes, as well as to embed a high mole fraction $\text{Al}_{0.98}\text{Ga}_{0.02}\text{As}$ layer graded to GaAs from both sides. The $\text{Al}_{0.98}\text{Ga}_{0.02}\text{As}$ layer should be selectively oxidized during the processing of the VCSELs,

¹ Similar structures were also designed for 1.3 μm VCSELs, but are not presented herein.

² As observed in Fig. 3(b), the thickness had to be modified to $3\lambda/4$ in order to obtain the desired optical mode profiles for the case of InP barrier devices.

primarily to confine the injection current to the active region and prevent shunt current between the ohmic contacts.

With respect to the basic design constraints required for device operation, other parameters such as various thicknesses and mole fractions were varied such that the TE_{00} mode is the dominant mode with a peak field intensity in the QWs region, while all the other TE and TM modes have negligible intensity in that region. The TE_{00} mode amplitudes in the growth direction for both devices of Fig. 3 are shown in Fig. 4 with the profile of refractive index along the same axis.

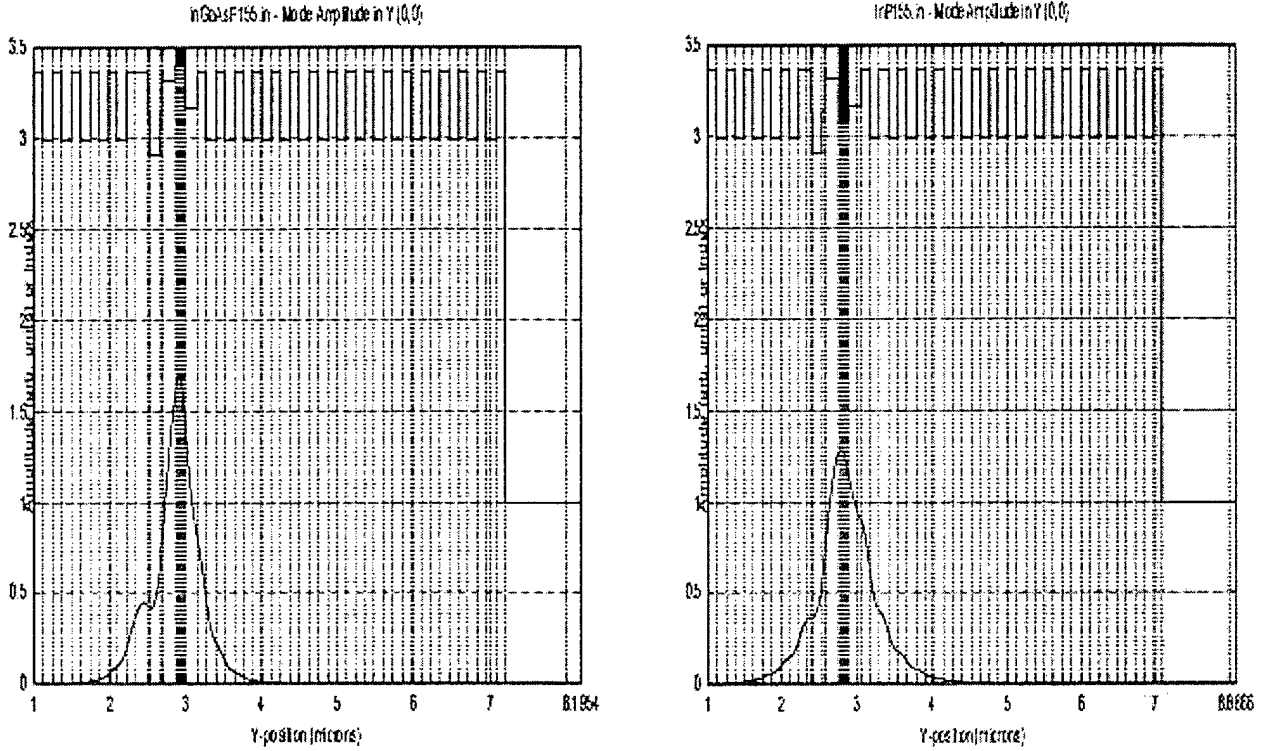


Fig. 4: The TE_{00} mode field intensity profile (a.u.) and the profile of the refractive index along the growth direction for VCSELs (a) and (b) in Fig.3.

2.4 Fabrication

The fabrication of the VCSELs was planned in four phases, the first of which is already accomplished and the others are underway.

Phase I of the fabrication was the growth of n- and p-doped DBRs on non-patterned GaAs substrates. This phase is primarily for the calibration of the growth conditions for optimum DBR structures. The measured reflectances for 12 periods of n-doped DBR and five periods of p-doped DBR is presented in Fig. 5. The n-doped DBR shows excellent wavelength selectivity with a maximum reflectivity of 87% at 1559 nm and a full-width-

at-half-maximum of 172 nm. The top DBR sample also includes the $3\lambda/4$ oxidation layer of the VCSELs with InP barriers. The graded AlGaAs layer comprises alternating layers of $\text{Al}_{0.80}\text{Ga}_{0.20}\text{As}/\text{GaAs}$, i.e., chirped superlattice structures. As can be observed in Fig. 5(b), the maximum reflectivity in this case is about 50 nm more than what expected, which will be recalibrated in the growth of the full laser structure in Phase III, as follows.

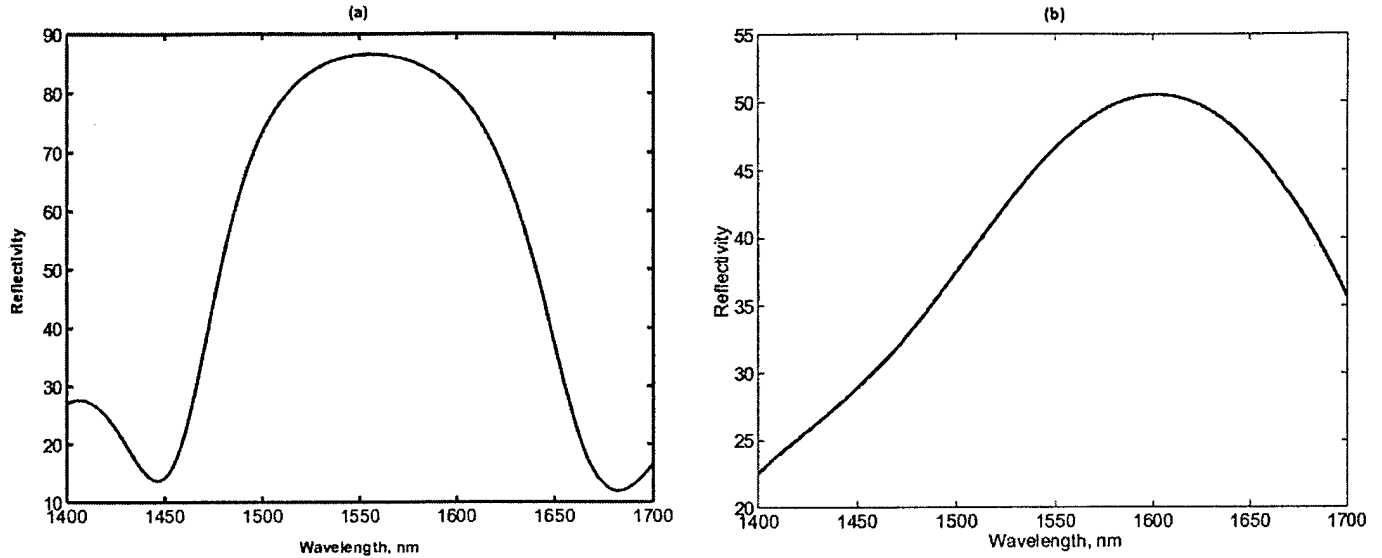


Fig. 5: Measured reflectivity of: (a) 12 periods of n-doped AlGaAs/GaAs DBRs; (b) 5 periods of p-doped AlGaAs/GaAs DBRs.

2.5 Future work

In Phase II of the fabrication, the two InP-based λ -cavity structures will be grown on non-patterned InP substrates and the compressive-strained barriers and tensile-strained wells will be calibrated on InP for lasing at 1550 nm. However, the main theoretical and experimental challenge of the project is tailoring these results in Phase III such that InP-based active regions with same photo-emission spectra can be grown on highly lattice-mismatched patterned GaAs substrates. Photoluminescence data appears to be the best to study the optoelectronic properties of strongly strained InGaAsP quantum wells on patterned GaAs substrates. Obviously, Phase IV will be processing and characterization of the VCSELs experimental benchmark

2.6 Conclusion

Design of 1.30-1.55 μm InGaAsP VCSELs with AlGaAs/GaAs DBRs to be grown on GaAs patterned substrate has been completed. The growth and calibration of the designed bottom DBR and the top DBR mirrors with chirped superlattice graded oxidation layer have been concluded. Growth of the active region cavity on InP substrates, followed by the growth of the full VCSEL structure are in progress.

3. References

1. P. R. Villeneuve, S. Fan and J. D. Joannopoulos, *Phys. Rev. B* **54**, 7837(1996)
2. O. Painter, R. Lee, A. Scherer, A. Yariv, J. O'Brien, P. Dapkus, I. Kim, *Science* **284**, 1819(1999)
3. W. D. Zhou, J. Sabarinathan, B. Kochman, E. Berg, S. Pang, P. Bhattacharya, *Electronics Letters* **36**, 1541(2000)
4. D. Bryne, A. Brounds, F. Case, R. Tiberio, B. Whitehead, E. Wolf, *J. Vac. Sci. Technol. B* **3**, 268(1985)
5. S. J. Pearton, W. S. Hobson, A. F. J. Levi, *Plasma Sources Sci. Technol.* **3**, 19(1994)
6. H. Gebretsadik, K. Kamath, K. K. Linder, X. Zhang, P. Bhattacharya, C. Caneau, R. Bhat, "Growth of high-quality GaAs/AlAs Bragg mirrors on patterned InP-based quantum well mesa structures", *Appl. Phys. Lett.* **71**, 581(1997)
7. H. Gebretsadik, O. Qasaimeh, H. Jiang, P. Bhattacharya, C. Caneau, R. Bhat, "Design and realization of 1.55 μ m patterned vertical cavity surface emitting laser with lattice-mismatched mirror layers", *J. Lightwave Tech.* **17**, 2595(1999)